Abstract

This paper deals with a permanent magnet brushless DC generator (PMBLDCG) based stand-alone wind energy conversion system (WECS) for small scale power generation. A buck-boost DC-DC converter is used for controlling the PMBLDCG speed to achieve optimum energy output from the wind turbine without sensing wind speeds. An uncontrolled diode bridge rectifier is used between PMBLDCG terminals and input of a buck-boost converter. The buck boost converter feeds the generated power to a battery energy storage system (BESS) and consumer loads. A single-phase voltage source inverter (VSI) is used at the DC bus to feed regulated voltage and frequency AC supply to consumer loads. The performance of the voltage and frequency controller is demonstrated as a maximum power point tracker, a load leveler, an active filter along with a voltage and frequency controller.

Keywords: Permanent Magnet Brushless DC Generator, Buck Boost Converter, Battery, Wind Power, Maximum Power Point Tracking.

DOI: http://dx.doi.org/10.4314/ijest.v4i1.8S

1. Introduction

Permanent magnet machines are known as a PM brushless DC (PMBLDC) machines based on the shape of electromotive force (EMF) induced in their stator winding (Gieras and Wing 2002). Due to the surface mounted PM on the rotor of PMBLDC machine, the shape of EMF induced at stator terminals are trapezoidal in nature. In recent years, wind energy conversion systems (WECS) are installed worldwide to deal with acute shortage of electrical energy (David et al 2001, Simoes and Farret 2004, Bhadra et al 2004, Ackermann 2005). The permanent magnet brushless DC generator (PMBLDCG) has 15% higher power density than PMSM machines (Lai and Chan 2007) and due to trapezoidal EMF, the rectified DC output voltage has reduced ripples. This reduces the size of the machine for a given power rating as compared to conventional synchronous machines and induction machines. These aspects provide PMBLDCG, a potential candidate for wind energy conversion systems (WECS) (Ojo et al 1997). Some research publications have reported on use of PMBLDCG for power conversion [Kim et al 2007, Krishnan and Rim 1990, Zhuoran et al 2009, Milivojevic et al future]. However, there is a limited attempt on stand-alone WECS using PMBLDCG. Therefore, this work focuses on using a PMBLDCG for stand-alone WECS.

This paper deals with a voltage and frequency controller (VFC) for a permanent magnet brushless DC generator based stand-alone wind energy conversion systems (SWECS’s). The VFC is realized using a buck-boost converter and a single-phase voltage source inverter (VSI) with a battery energy storage system at DC link.
2. System Configuration and Principle of Operation

Fig. 1 shows the block diagram of system configuration of PMBLDCG based stand-alone WECS. This proposed stand-alone WECS consists of a PMBLDCG, a three phase diode bridge rectifier, a low pass LC filter, a buck-boost DC-DC converter, a BESS and a vector controlled pulse width modulated (PWM) single-phase VSI along with single-phase consumer loads. The output voltage of PMBLDCG is trapezoidal in nature and its frequency is also varying due to change in the generator speeds for extracting the maximum power from the wind turbine. Therefore it cannot be used to feed consumer loads. A buck-boost DC-DC converter is operated in power controlled mode to extract the maximum wind power through a PMBLDCG. The active switch of a buck-boost converter is controlled by modulating its duty cycle under variation in wind speeds to extract the maximum power from the wind turbine. The VSI is connected to consumer loads through a LC filter to absorb high frequency switching ripples. A BESS is used at the DC bus for the load leveling. During high wind speeds and less loads, the BESS absorbs the difference of generated power and consumer loads. When the wind power is low or consumer load demand is high, the BESS supplies the deficit load demand.

3. Design of Voltage and Frequency Controller

The design of VFC consists of selection of the battery voltage and its rating, design of a buck-boost converter, selection of single-phase VSI and design of output LC filter as follows.

3.1 Choice of Battery Voltage and its Rating: The choice of BESS voltage depends on rated line voltage of PMBLDCG and the required load terminal voltages. For satisfactory operation of the load side VSI, the minimum battery voltage must be more than peak of load terminal voltage as follows,

\[ V_{dc} \geq \sqrt{2}V_o \]  

where \( V_o \) is the rms value of load voltage.

The storage capacity of BESS is given in kWh and it is based on wind regime at the site, diversity of connected consumer loads and loss of load expectation. In this study, a 5kWh BESS is used with a battery voltage of 408 V. The mathematical modeling of BESS is done as given in (Singh and Kasal 2008) and selected parameters are given in Appendix.

3.2 Design of Buck-Boost Converter: The output voltage \( V_{dc} \) of the buck-boost converter is given as,

\[ V_{dc} = D \frac{V_c}{(1-D)} \]  

An inductance \( L \) of the buck boost converter restricts the peak to peak current ripple \( (\Delta I_L) \) within specified value for the given switching frequency \( (f_s) \). The design equation for the inductor is given as,

\[ L = (1-D)V_c / \{f_s(\Delta I_L) \} \]
where $V_c$ is the average output of the uncontrolled bridge rectifier and $D$ is the duty cycle of the buck boost converter. The buck-boost converter is designed for a 3 kW, 7 Nm rated torque PMBLDCG (data given in Appendix) with a base DC link voltage of $V_b = 408$ V at rated PMBLDCG speed (400 rad/s). Other design data are $f_t = 40$ kHz, $I_L = 9.42$ A, $\Delta I_t = 0.9$ A (10% of $I_L$). The design value of an inductor is obtained as $L = 6$ mH. The maximum current through the active switch and the diode is according to maximum current through the inductor. Similarly the maximum voltage which the switch and diode have to block is the battery voltage. Therefore the desired voltage ratings of IGBT and diode are considered of 600 V.

3.3 Selection of Single Phase VSI: For feeding 3 kW, 0.8 lagging power factor loads using a single-phase two-leg VSI, the maximum current through switching device is given as $1.25 (I_{pp} + 2 I_{VSI})$ (Singh et al 2004). Considering 5% peak to peak ripple in the VSI current and $I_{VSI}$ equals to 16 A. Therefore, an IGBT of 35 A, 600 V is selected for a two-leg VSC as a VSI.

3.4 Design of LC Filter: To obtain sinusoidal load voltage, a LC filter is used between the load and VSI terminals. Further, the resonant frequency of LC filter should be well below the lowest harmonic present in the PWM output voltage and it should be very high than the desired fundamental frequency (Steinke 1999). The PWM frequency of single-phase VSI is selected 10 kHz and fundamental load voltage frequency is 50 Hz. For the considered resonant frequency of 1000 Hz, the selected value of LC filter are as $L_f = 3$ mH and $C_f = 10 \mu F$.

4. Control Algorithm

The objectives of control of the PMBLDCG based stand-alone WECS are maximum wind power extraction and to feed uninterruptable power to the targeted consumers. The control algorithm is divided in to two parts i.e. one deals with the control of a buck-boost converter to control over the PMBLDCG power and the other section deals with control of single-phase VSI to regulate load voltage and frequency. The buck-boost converter is operated in power control mode by modulating the duty cycle of its active switch in PWM mode. Three phase diode bridge rectifier DC output voltage ($V_o$) and its current ($i_o$) are sensed and filtered using a low pass filter with a cut-off frequency of 20 Hz and multiplied to obtain the instantaneous PMBLDCG power ($P_g$). The estimated $P_g(n)$ is compared with the already available sample of $P_g(n-1)$ stored at previous sampling time. The difference power is fed to a power controller to obtain the reference current ($i_0^{*}$). The reference current ($i_0^{*}$) and sensed current ($i_o$) are compared and fed to a proportional-integral (PI) controller to provide the reference modulation index (m) for a PWM controlled switch. This control algorithm does not require measurement of wind speed, rotor speed and sensing of PMBLDCG stator terminal voltages and currents for maximum power point tracking. Further, the buck-boost converter allows power generation under large variations in wind speeds. When there is a rise in the wind power, the modulation index (m) increases to absorb higher power and vice-versa. The single-phase VSI is controlled in vector controlled mode. The reference voltage signal of desired frequency ($V_o^{*}$) is compared with the sensed terminal voltage ($V_o$) and the error voltage ($v_{er}$) is processed through a PI controller to obtain the reference VSI current ($i_{VSI}^{*}$). The reference VSI current is compared with sensed VSI current ($i_{VSI}$) to obtain current error to feed the PWM controller block to generate the switching signals for insulated gate bipolar transistors (IGBT’s) of single-phase VSI. Basic equations used in modeling of the VFC are given as follows.

4.1 Maximum Power Point Tracking Control of PMBLDCG: The output voltage ($V_c$) of an uncontrolled rectifier is sensed and filtered. Similarly current ($i_s$) is sensed and filtered. The instantaneous generated power ($P_g$) is estimated at $n^{th}$ sampling instant as follows,

$$P_g(n) = V_c(n)i_s(n)$$

(4)

The estimated $P_g$ at $n^{th}$ sampling instant and $(n-1)^{th}$ sampling instant are compared and the difference of these powers are termed as $P_g(n)$. This $P_g(n)$ is the input to the power proportional- integral (PI) controller. The output of the power PI controller at the $n^{th}$ sampling instant is given as,

$$i_0^{*}(n) = i_0^{*}(n-1) + K_{ppg} [P_g(n) - P_g(n-1)] + K_{ig} P_g(n)$$

(5)

where $i_0^{*}$ is the reference current and $K_{ppg}$ and $K_{ig}$ are the proportional and integral gain constants of a PI controller respectively. The reference current $i_0^{*}$ and sensed current $i_s$ are compared and the current error ($i_{er}$) is amplified to obtain the desired modulation index (m) to control the duty cycle of an active switch of the buck-boost converter.

4.2 Control of Single Phase VSI: The objectives of controlling single-phase VSI are to regulate the output voltage and its frequency, to feed linear and non-linear loads. The reference load voltage is given as,

$$v_0^{*}(n) = V_m \sin(2\pi ft)$$

(6)

where $V_m$ is the maximum value of the reference AC voltage (i.e. 325V in this case) and $f$ is the reference frequency of the load voltage (i.e. 50Hz in this case).

The reference and sensed voltage are compared individually at $n^{th}$ sampling instant as follows,

$$v_{oc}(n) = v_0^{*}(n) - v_o(n)$$

(7)
This error voltage \(v_{out}\) are fed to a PI controller to obtain the reference VSI current \(i_{\text{ref}}\), which output at \(n^\text{th}\) sampling instant is given as follows,

\[
i_{\text{ref}}(n) = i_{\text{ref}}(n-1) + K_p \{v_{out}(n) - v_{out}(n-1)\} + K_i v_{out}(n) \quad (8)
\]

These reference VSI current \(i_{\text{ref}}\) and sensed VSI current \(i_{\text{vsi}}\) are compared and the current error \(i_{\text{vsi}}\) is amplified using gain \(K_i\) and amplified current error is fed to PWM controller with fixed frequency triangular carrier wave (10 kHz) of unit amplitude to generate the uni-polar switching signals for IGBT’s of single-phase VSI. The unipolar switching scheme is used to reduce the effective switching frequency of the IGBT’s and hence switching losses of VSI.

5. Modeling of PMBLDCG Based WECS

The PMBLDCG based stand-alone WECS is modeled in MATLAB R2007 using Sim-Power system toolbox and a discrete step solver. The modeling of electrical and mechanical parts of the WECS is given herewith and the parameters used in this study are given in Appendix.

5.1 Modeling of Variable Speed Wind Turbine: The mechanical power developed by a wind turbine in per unit system [2] is given as,

\[
P_m = 0.5 \rho A C_p(\lambda, \beta) v^3_{\text{wind}} \quad (9)
\]

where \(P_m\) is the power expressed for specific density of air \((\rho)\) and the swept area of turbine blades \((A)\). \(C_p\) is the power coefficient and \(v_{\text{wind}}\) refers wind speed in m/s.

To optimize the power output from a fixed pitch angle \((\beta)\) wind turbine, the turbine speed must vary as the wind speed changes to keep the tip speed ratio \((\lambda)\) constant. A generic equation is used to model the wind turbine \(C_p(\lambda, \beta)\). The equation used to model the wind turbine characteristics is as,

\[
C_p(\lambda, \beta) = C_1 \{e^{(\pi / \lambda)} - e^{-\pi / \lambda}\} + C_8 \lambda (1 / \dot{\lambda}) = \{1 / (\lambda - C_7 \beta)\} - \{C_8 / (\beta^3 + 1)\} \quad (10)
\]

In this model \(\beta=0\). The \(\lambda\) is defined as tip speed ratio (ratio of angular speed of turbine/wind speed) and \(C_1-C_8\) are wind turbine constants. The values of these constants are given in Appendix.

5.2 Modeling of PMBLDCG: From the equivalent circuit (shown in Fig. 1), the PMBLDCG is modeled in the form of a voltage equations (Putta Swamy et al 1995) given as,

\[
e_{\text{an}} = v_{\text{an}} - R i_a - p \lambda_a \quad (11)
\]

\[
e_{\text{bn}} = v_{\text{bn}} - R i_b - p \lambda_b \quad (12)
\]

\[
e_{\text{cn}} = v_{\text{cn}} - R i_c - p \lambda_c \quad (13)
\]

where \(R\) is the resistance of motor windings/phase and \(p\) is the derivative operator. The \(i_{a}, i_{b}\) and \(i_{c}\) are each phase currents, \(\lambda_{a}, \lambda_{b}\) and \(\lambda_{c}\) are flux linkages with each phase windings and \(e_{an}, e_{bn}\) and \(e_{cn}\) are phase to neutral induced emfs of PMBLDCG while \(v_{an}, v_{bn}\) and \(v_{cn}\) are the terminal voltages in respective phases defined as.

\[
v_{an} = v_{a0} - v_{n0}, v_{bn} = v_{b0} - v_{n0}, v_{cn} = v_{c0} - v_{n0} \quad (14)
\]

where \(v_{a0}, v_{b0}, v_{c0}\) are three phase and neutral voltages refered to zero potential at mid-point of DC link.

The flux linkages are represented as,

\[
\lambda_a = L_{ia} i_a + M (i_b + i_c) \quad (15)
\]

\[
\lambda_b = L_{ib} i_b + M (i_c + i_a) \quad (16)
\]

\[
\lambda_c = L_{ic} i_c + M (i_a + i_b) \quad (17)
\]

where \(L_i\) is the self inductance of each phase winding and \(M\) is the mutual inductance between each phase winding.

The PMBLDCG has no neutral connection and therefore it can be written as,

\[
i_a + i_b + i_c = 0 \quad (18)
\]

By substituting Eqs. (15-17) in Eqs. (11-13), the volt ampere equations results in state space current derivitve form as [14],

\[
p_{ia} = 1 / (L_s + M) \{e_{an} - i_a R - v_{an}\} \quad (19)
\]

\[
p_{ib} = 1 / (L_s + M) \{e_{bn} - i_b R - v_{bn}\} \quad (20)
\]

\[
p_{ic} = 1 / (L_s + M) \{e_{cn} - i_c R - v_{cn}\} \quad (21)
\]

Three-phase generated voltage \(e_{an}, e_{bn}\) and \(e_{cn}\) are in trapezoidal shape. For this, three-phase back emf are expressed as a function of rotor position \((\theta_r)\) as,

\[
e_{an} = k_{fa}(\theta_r) \omega_r, e_{bn} = k_{f\beta}(\theta_r) \omega_r, e_{cn} = k_f(\theta_r) \omega_r, \quad (22)
\]

where \(f_a(\theta_r) = 1\) for \(0 < \theta_r < 2\pi / 3\), \(f_\beta(\theta_r) = (6/\pi)(\pi - \theta_r)\) for \(2\pi / 3 < \theta_r < \pi\), \(f_f(\theta_r) = 1\) for \(\pi < \theta_r < 5\pi / 3\), \(f_f(\theta_r) = (6/\pi)(\theta_r - 2\pi)\) for \(5\pi / 3 < \theta_r < 2\pi\), \(f_\beta(\theta_r) = 1\) for \(\pi < \theta_r < 5\pi / 3\), \(f_f(\theta_r) = (6/\pi)(\theta_r - 2\pi)\) for \(5\pi / 3 < \theta_r < 2\pi\).

Similarly, \(f_a(\theta_r)\) and \(f_\beta(\theta_r)\) may be expressed as a function of rotor position \(\theta_r\). The developed electromagnetic torque is expressed as follows.
Te = \{k_f f_a(\theta) i_a + k_b f_b(\theta) i_b + k_c f_c(\theta) i_c\} \tag{27}

and \[ p_\omega = \left(\frac{P}{2}\right)(\frac{T_w - B\omega}{J}) \tag{28} \]

where \( T_w \) is the wind turbine torque, \( K_b \) is back-emf constant, \( B \) is the coefficient of friction and \( J \) is the combined inertia of wind turbine and a PMBLDCG.

6. Results and Discussion

The performance of the VFC for a PMBLDCG based stand-alone WECS with BESS is evaluated for different conditions under change in loads and change in wind speeds. The performance of the VFC is observed in terms of generated voltage and current for phase ‘a’ of PMBLDCG, wind speed (\( V_w \)) in m/s, generator speed (\( \omega_r \)) in rad/s, buck boost inductor current (\( i_L \)), battery voltage (\( V_b \)), load voltage (\( V_o \)), load current (\( i_o \)), generated power (\( P_g \)), battery charging power (\( P_b \)) and load power (\( P_o \)).

6.1 Performance of VFC Under Fall in Wind Speed: Fig. 2 shows the performance of VFC under fall in wind speed from 12 m/s to 9 m/s. A fixed 3 kW linear consumer load is supplied from single-phase VSI. Till 2 s, the wind speed is 12 m/s and there is a rated power generation through PMBLDCG. At 2 s, there is a reduction in wind speed and also in generated power. Due to reduction in wind speed, PMBLDCG speed also falls to achieve MPPT and it reaches 310 rad/s. It is observed that BESS is under floating state till wind speed is 12 m/s and starts discharging when the wind speed falls to 9 m/s. The performance of VFC demonstrates function of a load leveler along with a voltage and frequency controller.

6.2 Performance of VFC during Rise in Wind Speed: Fig. 3 shows the performance of VFC during rise in wind speed from 9 m/s to 12 m/s. Till 2 s, the wind speed is 9 m/s and fixed linear load is connected across load bus. The BESS is discharging due to higher load demand than the generated power. At 2 s, wind speed increases to 12 m/s and there is a rise in generated power. This leads to battery in floating state. As wind speed increases, there is rise in PMBLDCG speed to track maximum power point.

6.3 Performance of VFC at Fixed Wind Speed under Varying Linear Loads: Fig. 4 shows the performance of a VFC at constant wind speed (12 m/s) and feeding linear loads. Till 3.5 s, a 3 kW, 0.89 lagging power factor load is supplied from a VSI. Due to the rated wind speed, there is rated power generated and it leads to an ideal mode of a battery. At 3.5 s, due to complete removal of loads, the load power is zero and it causes battery to charge with rated generated power. The load is connected back on the load bus at 3.6 s and battery comes in floating state. It is observed that even a transition of loads from minimum to rated conditions, the load voltage is sinusoidal. It demonstrates the performance of VFC as a load leveler along with a voltage and frequency controller.
6.4 Performance of VFC at Fixed Wind Speed under Non-Linear Loads: Fig. 5 show the performance of VFC under non-linear consumer load at load bus and wind speed is kept constant at 12 m/s. A single-phase diode rectifier feeding resistive-inductive element is used as a non-linear load. Till 1.9 s, a 2 kW load is present on load bus and terminal voltage is observed ripple free. At 1.9 s, load is completely removed from load bus. It is observed that even with minimum current on load bus, the load terminal voltage is constant and sinusoidal. However with these conditions, the battery is absorbing the generated power. The load is switched in again at 2 s and there is a reduction in BESS charging power. Even with such a large load current perturbations, the observed terminal voltage is constant. The harmonic spectra of load terminal voltage $V_o$ and load current $i_o$ are shown in Fig. 6(a-b). The total harmonic distortion of load terminal voltage is within IEEE 519-1992 standard limit of 5% even the load current THD is 44.29%.

Figure. 3 Performance of VFC under rise in wind speed (9 m/s-12 m/s) at fixed linear load
7. Conclusions

A new topology of PMBLDCG based variable speed stand-alone WECS has been proposed using a DC-DC buck-boost converter, a battery energy storage system and a single-phase voltage source inverter. A voltage and frequency controller has been realized using vector control approach to achieve MPPT, load leveling, harmonic elimination along with voltage and frequency control at load terminals. A mechanical sensor-less approach has been used for achieving MPPT for small scale applications. The performance of VFC has been found satisfactory as a load leveler, as a maximum power point tracker, an active filter along with voltage and frequency control.
Figure. 5 Performance of VFC at fixed wind speed (12 m/s) under non-linear load

Figure. 6 Harmonic spectra (a) load terminal voltage $V_L$, (b) load current $i_L$

Appendix

A. Permanent Magnet Brushless Generator- 3 kW, 7 Nm, 4000 RPM, 6-Pole, Stator Resistance per phase=2.17 ohm, Phase Inductance=6.6 mH, $J=0.002$ Kg/m², phase Voltage at rated speed=204V.

B. Variable Speed Wind Turbine- 3.5 kW, $C_p_{max}=0.48$, $\lambda_m=8.1$, $C_1=0.5176$, $C_2=116$, $C_3=0.4$, $C_4=5$, $C_5=21$, $C_6=0.0068$, $C_7=0.008$, $C_8=0.035$. Gear Box ratio=10.

C. Buck-Boost Converter- $L=6$ mH, Diode conduction drop=0.7V, PWM Switching Frequency=40 kHz, $L_m=3$ mH, $C=1$ µF.

D. Battery- $V_{oc}=408$V, $V_{ocmin}=459$V, $V_{ocmax}=357$V, $C_b=433$ F, $R_{sb}=10$ kΩ, $R_s=0.1$Ω.

E. Specifications of VSI LC Filters- $L_f=5$mH, $C_f=10$µF.
References


Bhim Singh was born in Rahamapur, (UP), India, in 1956. He received his B.E. from the University of Roorkee, Roorkee, India, in 1977 and his M.Tech and Ph.D. from the Indian Institute of Technology (IIT) Delhi, New Delhi, India, in 1979 and 1983, respectively. In 1983, he joined the Department of Electrical Engineering, IIT Delhi, as an Assistant Professor. He became an Associate Professor in 1994 and a Professor in 1997. He has guided 38 Ph.D. dissertations, 120 M.E./M.Tech./M.S.(R) theses, and 60 BE/B.Tech. Projects. His areas of interest include power electronics, electrical machines and drives, renewable energy systems, active filters, FACTS, HVDC and power quality. Dr. Singh is a Fellow of the Indian National Academy of Engineering (INAE), the National Science Academy (NSc), the Institute of Engineering and Technology (IET), the Institution of Engineers (India) (IEI), and the Institution of Electronics and Telecommunication Engineers (IETE). He is also a life member of the Indian Society for Technical Education (ISTE), the System Society of India (SSI), and the National Institution of Quality and Reliability (NIQR). He received the Khosla Research Prize of the University of Roorkee in the year 1991. He was the recipient of J.C. Bose and Bimal K. Bose Awards of the Institution of Telecommunication Engineers (IETE) for his contributions in the field of power electronics in the year 2000. He was also a recipient of the Maharashtra State National Award of the Indian Society of Technical Education (ISTE) in recognition of his outstanding research work in the area of Power Quality in the year 2006. He received the PES Delhi Chapter Outstanding Engineer Award for the year 2006. He was the General Chair of the IEEE International Conference on Power Electronics, Drives and Energy Systems (PEDES’2006) and (PEDES’2010) held in New Delhi.

Shailendra Sharma was born in Indore, (MP), India, in 1972. He received his M.E. degree in Electrical Engineering with specialization in power electronics from the Shri Govindram Seksaria Institute of Technology and Science (SGSITS), Indore, India in 2004. He has five years of industrial experience as an Erection and Commissioning Engineer with M’s Dhar Textile Mill Ltd, Indore. Presently, he is an Assistant Professor in Department of Electrical Engineering, SGSITS, Indore, Madhya Pradesh, India. His fields of interest include power electronics, electric drives, power quality, and renewable energy systems. Mr. Sharma is an associate member of the Institution of Engineers (India).

Received January 2012
Accepted February 2012
Final acceptance in revised form March 2012